

Form PTO-1449		ATTORNEY DOCKET NO. <b>AUS920010714US1</b>	SERIAL NO.		
<b>LIST OF PRIOR ART CITED BY APPLICANT</b> <i>(Use several sheets if necessary)</i>		APPLICANT <b>DeWitt et al.</b>	10/04/2002 S. PRO 11002 01/12/02		
		FILING DATE		GROUP ART UNIT	
<b>U.S. PATENT DOCUMENTS</b>					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
KPL	4,205,370	05/27/1980	Hirtle	364/200	—
KPL	4,598,364	07/01/1986	Gum et al.	364/300	—
KPL	5,446,876	08/29/1995	Levine et al.	395/184.01	—
KPL	6,173,395 B1	01/09/2001	Wisor et al.	712/236	—
KPL	AUS920010717US1		Jimmie Earl DeWitt, Jr., et al.	—	1/14/02
<b>FOREIGN PATENT DOCUMENTS</b>					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO
KPL	WO 99/54809	10/28/1999	WIPO	—	—
<b>OTHER PRIOR ART (including author, title, date, pertinent page, etc.)</b>					
KPL	<i>AIX Version 4.3 Kernel and Subsystems Technical Reference, Vol. 1, "pinu Kernel Service", IBM Corp., 1997.</i>				
KPL	<i>Itanium™ Processor Microarchitecture Reference for Software Optimization, Intel Corp., 03/2000, pp. 1-28.</i>				
KPL	<i>Itanium™ IA-64 Architecture Software Developer's Manual, Vol. 4: Itanium™ Processor Programmer's Guide, Revision 1.1, Intel Corp., 07/2000, pp. 6-1 to 6-32.</i>				
KPL	<i>Itanium™ IA-64 Architecture Software Developer's Manual, Vol. 1: IA-64 Application Architecture, Revision 1.1, Intel Corp., 07/2000, pp. 2-1 to 2-8, 3-1 to 3-20, 4-1 to 4-35, 9-1 to 9-16, 10-1 to 10-16.</i>				
KPL	<i>Itanium™ IA-64 Architecture Software Developer's Manual, Vol. 2: IA-64 System Architecture, Revision 1.1, Intel Corp., 07/2000, pp. 2-1 to 2-4, 3-1 to 3-22, 5-1 to 5-36, 7-1 to 7-14.</i>				
DATE CONSIDERED 2/16/05		EXAMINER <i>JL</i>			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.					